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Intellectual Output 7 - Dependability and Safety

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0.1	30.11.2021	Initial Version	Ali Behravan
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Contributors

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1 Introduction

Course Plan Template can help the partners to prepare the course materials that help us to organize the materials and contents of courses in the SkoPS project in standard form. Some other Forms and templates for the student assessment Plan and Evaluation Questionaries Plan for each Course can be added to this Template.

1.1 Abstract

Dependability is the ability of a system to deliver the intended service, including fault-tolerance techniques. In Real-time computing, dependability refers to service provision at a particular time. Functional safety is concerned with the safety-critical systems and concentrates on the characteristics of the extra systems added to a system with the purpose of making its operation safe. The main concept of the dependability theory is the threats, the attributes, and the techniques used to enforce dependability.

1.2 Purpose of the document

The purpose of this document is to have the same template for all courses. In this template, for each course, some features are considered.

1.3 Relation to other deliverables

This Report is part of the D0.7, which is the general structure of each course.

2 Template

	Course Plan	Template	
Course ID and Title:	Dependability and Safety	,	
Course Duration:	2/3 months	Course ECTS:	1/1.5
Leading Organization:	University of Siegen (USI))	·
Course Media:	Text / Text File / Video /	Website/ Internet	
Laboratory (Yes/No)	Yes		
Course Description:	_		

Safety-critical applications require high reliability in computing and electronic systems, which is achieved by designing fault-tolerant systems. This module introduces the design and analysis of reliable computing systems and is represented in 5 weeks. This course presents the fundamentals of fault-tolerant systems and fault-tolerance tools at both hardware and software levels, e.g., redundancy and re-execution. It will give an understanding of how to detect faults, design computing components to tolerate faults, and measure the reliability of systems. This course will help students design and analyze reliable computing systems. This course includes five chapters.

It starts with introductory information about the faults, errors, failures, etc. In Chapter 2, a preliminary description of fault-tolerance techniques is given. Chapter 3 introduces the requirements of dependability. Chapter 4 presents hardware redundancy for fault tolerance and probabilistic tools to measure reliability. Chapter 5 describes fault-tolerance techniques for software systems.

Course Materials and Equipment (Prerequisite)

- This course is suitable for graduate and senior-level undergraduate students.
- Students should have basic knowledge of ICT systems design, analysis, and probability theory.

Teaching and Learning Activities:

- Providing lectures and content
- Using graphs, images, and videos to enhance the understanding of the subject
- Solving different examples in modeling and evaluation to clarify the problem
- Evaluation activities

Course activities:

- Revision and advancement of existing teaching/learning materials
- Type: Open Educational Resource (OER)
- Discussion and interactive activities

Course Objectives:

- Providing the introductory knowledge, terminologies, and concepts applied in the specification, design, evaluation, and principles of fault-tolerant operations
 - Designing robust and resilient systems.
- Overviewing the dependability in the field of computing systems.
- Increasing the awareness of the potential impact of failures, dependability, and safety in the industrial environment.

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4.21 Keliadility Evaluation Techniques: Iviarkov Models
4.21.1 The Markov model for a duplex system with an inactive spare
4.21.2 The Markov model for a duplex system with an inactive
4.21.3 The Markov model for a duplex system with repair
4.22 Fault-Tolerance Processor-Level Techniques
4.22.1 Watchdog Processor
4.22.2 Simultaneous Multithreading for Fault Tolerance
4.23Byzantine Failures
5. Software Fault Tolerance
5.1 Software Fault Tolerance
5.2 Acceptance Tests
5.2.1 Timing checks
5.2.2 Verification of Output

5.2.3 Range Checks

Laboratory Description and Equipment:

To cover the online format of the module design, remote labs are also designed, which are aligned with the pandemic situations. Bridge the gap from theory to practical implementation by performing a practical experiment in the lab. The practical part depends on the knowledge of practical tasks of the lecture contents (e.g., programming of embedded Systems with microcontrollers, scheduling, memory management, and time analysis). The Embedded Control Lab can be held on by the university of Siegen using an online platform.

Course References:

- [1]. Shooman, M.L., 2003. Reliability of computer systems and networks: fault tolerance, analysis, and design. John Wiley & Sons.
- [2]. Koren, I. and Krishna, C.M., 2020. Fault-tolerant systems. Morgan Kaufmann.
- [3]. Sorin, D.J., 2009. Fault-tolerant computer architecture. Synthesis Lectures on Computer Architecture, 4(1), pp.1-104.
- [4]. Avizienis, A., Laprie, J.C. and Randell, B., 2001. Fundamental concepts of dependability. Department of Computing Science Technical Report Series.
- [5]. Obermaisser, R. and Peti, P., 2006, June. A fault hypothesis for integrated architectures. In 2006 International Workshop on Intelligent Solutions in Embedded Systems (pp. 1-18). IEEE.

Evaluation and Assessment Methods:

Assessments take various forms, including Quizzes and Multiple-choice questions after each module. The course will be passed by 80% of the quizzes.

The tasks lead to the production of the intellectual output and the applied methodology.

The methodology in creating the SkoPS courses will be as follows: The leading partner is mainly responsible for preparing the materials and will distribute tasks to the other partners based on their experiences and skills. All other partners will review the prepared materials and provide feedback to improve the materials' quality. A systematic review process with all partners involved will further enhance the quality of the courses. Right at the beginning of the project (directly after the kick-off meeting), a requirement analysis will be carried out by all project partners to precise the content of the courses which are outlined in this proposal. A second meeting will be conducted six months after the kick-off meeting as an internal course development Hackathon. Each partner will invite one or two external experts for this Hackathon to get their feedback for improving the quality of the course.